

# Latch-Up Detection And Cancellation In CMOS VLSI Circuits By Phillippe O. Pouliquen

By Phillippe O. Pouliquen

**Open Positions / Join Our Team / Home - ams AG -**

Active Noise Cancellation Demo; Ambient Light Sensor & Proximity Detection; Home / Join Our Team / Open Positions. Open Positions; Working with us; Living

<http://ams.com/eng/Join-Our-Team/Open-Positions>

**Lesson(s) Learned: Fault Protection - NASA -**

Management and coordination of fault detection, "Safing" is defined as a general purpose fault response which results in the cancellation of non (latch-up

<http://llis.nasa.gov/lesson/772>

**Hirel micro - Parts Management Santa Clara CA, -**

HRM has expanded its services to include comprehensive parts management, counterfeit detection and material verification, ESD and Latch-up etc.

<http://www.manta.com/c/mttcx11/hirel-micro-parts-management>

**xiaofeng fan profiles | LinkedIn -**

View the profiles of professionals named xiaofeng fan on LinkedIn. Cancel. 12 of 12 Current Engineer at Apple Past Senior Engineer ESD / Latch-Up at Micron

<https://www.linkedin.com/pub/dir/xiaofeng/fan>

**Chapter 5 GPS Based Relative Navigation -**

navigation benefits from a high level of common error cancellation. may further be protected by an error detection and a latch-up protection must be

[http://link.springer.com/content/pdf/10.1007/978-1-4614-4541-8\\_5.pdf](http://link.springer.com/content/pdf/10.1007/978-1-4614-4541-8_5.pdf)

**sceas.csd.auth.gr -**

sceas.csd.auth.gr

[http://sceas.csd.auth.gr/php/conferences.php4?conf\\_id=13454](http://sceas.csd.auth.gr/php/conferences.php4?conf_id=13454)

**Patent US8184801 - Acoustic echo cancellation for -**

In a mobile terminal device having a processing unit and a memory unit, an acoustic echo cancellation and a microphone beamforming are provided.

<http://www.google.com/patents/US8184801>

**Phillippe O. Pouliquen - Padgett-martin Technology -**

Award Title Agency Phase Award amount Start Date End Date; Latch-Up Detection and Cancellation in CMOS VLSI Circuits Padgett-martin Technology Principal Investigator

<https://sbirsource.com/sbir/people/18856-philippe-o-pouliquen>

**000445 ESD/ Latch- up Test Engineer (f/m) / Open -**

Active Noise Cancellation; Audio Front-Ends; Headphone Amplifiers; Operational Amplifiers; Phones Gesture, Color and Proximity Detection; Light-to-Digital Sensors;

<http://ams.com/eng/Join-Our-Team/Open-Positions/000445-ESD-Latch-up-Test-Engineer-f-m>

**Patent US5424881 - Synchronous read channel - -**

together with the error-tolerant sync mark detection and the ability to Latch-up recovery in  
- Terms of Service - About Google Patents  
<http://www.google.com/patents/US5424881>

**Amazon.fr: Phillippe O. Pouliquen: Livres, -**

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<http://www.amazon.fr/Phillippe-O.-Pouliquen/e/B00LLKSKOG>

**Delbert R Cecchi, Inventor, Rochester, MN, US - -**

Delbert R Cecchi's Inventor Triple-well CMOS devices with increased latch-up immunity and methods 2008: Method and apparatus for detection and prevention of  
<http://www.patentbuddy.com/Inventor/Cecchi-Delbert-R/165545>

**Analog Devices New Product Express | DigiKey -**

AD9361: RF Agile Transceiver. Blind spot detection; Self-parking; Electronic bumper; AD8284 . High Voltage Latch-Up Proof, Single SPDT Switch.  
<http://www.digikey.com/product-highlights/us/en/analog-devices-npe-vol6/4032>

**IEEE Xplore - Conference Table of Contents -**

Frequency degradation is monitored with a beat frequency detection system of 52dBc up to Nyquist up to 23Gb/s through ISI cancellation in the input latch  
[http://ieeexplore.ieee.org/xpl/tocresult.jsp?sortType%3Dasc\\_p\\_Sequence%26filter%3DAND\(p\\_IS\\_Number%3A5205281\)%26pageNumber%3D2%26rowsPerPage%3D75&pageNumber=1](http://ieeexplore.ieee.org/xpl/tocresult.jsp?sortType%3Dasc_p_Sequence%26filter%3DAND(p_IS_Number%3A5205281)%26pageNumber%3D2%26rowsPerPage%3D75&pageNumber=1)

**7th European Conference on POWER by liuqingzhan -**

7th European Conference on POWER.doc Download legal documents  
<http://www.docstoc.com/docs/34130849/7th-European-Conference-on-POWER>

**Fast on-detector integrated signal processing -**

Its main task is to prepare the signal for the tail cancellation by removing which is capable of tracking all of the up to 16,000 as error detection is  
<http://www.sciencedirect.com/science/article/pii/S0168900204000816>

**Hi-Reliability Microelectronics , Inc.: Private -**

Hi-Reliability Microelectronics , Inc. company research & investing information. ESD, and latch-up services. Cancel. Submit. Your requested  
<http://www.bloomberg.com/research/stocks/private/snapshot.asp?privcapid=42625671>

**SQL Server Wait Types | Senior DBA -**

Jan 10, 2014 LATCH\_SH, LATCH\_UP, LATCH\_EX, Occurs while CLR waits for deadlock detection to complete. can read more about SQL Server wait types here.  
<https://seniordba.wordpress.com/2014/01/11/sql-server-wait-types/>

**IGBT Gate Driver Solutions for Low and Medium -**

as well as the integration of additional blocks such as bootstrap diodes or charge pumps for power supply and V CE detection Latch Up Immunity cancel reply  
<http://www.powerguru.org/igbt-gate-driver-solutions-for-low-and-medium-power-applications/>

**SQL Server Latch & Debugging latch time out -**

Used during read IO during write IO when torn page detection Time out occurred while waiting for buffer latch Break up of above warning. type . The latch  
<http://mssqlwiki.com/2012/09/07/latch-timeout-and-sql-server-latch/>

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<http://www.amazon.com/Phillippe-O.-Pouliquen/e/B00LLKSKOG/>

**Catastrophic Failure and Fault Tolerant Design of -**

Catastrophic Failure and Fault Tolerant Design of IGBT Power Electronic Converters an Overview - Free download as PDF File (.pdf), Text file (.txt)

<https://www.scribd.com/doc/259206781/Catastrophic-Failure-and-Fault-Tolerant-Design-of-IGBT-Power-Electronic-Converters-an-Overview>

**Latest Enhancements to Electrical Fault -**

latch up etc. Irradiation of infra Long history on cameras Still improving detection performance Improvement of active noise cancellation function (2)

[http://www.eaq.com/cmss\\_files/attachmentlibrary/Latest-enhancements-to-electrical-fault-localization.pdf](http://www.eaq.com/cmss_files/attachmentlibrary/Latest-enhancements-to-electrical-fault-localization.pdf)

**Latch-Up Detection and Cancellation in CMOS VLSI -**

ADA399884. Title : Latch-Up Detection and Cancellation in CMOS VLSI Circuits. Descriptive Note : Final technical rept. 1 Jun 1999-20 Jun 2000. Corporate Author

<http://oai.dtic.mil/oai/oai?verb=getRecord&metadataPrefix=html&identifier=ADA399884>

**Publications 2002-1997 -**

Publications 2002-1997. Publications 1996-1991

<https://www.ims-bordeaux.fr/fr/recherche/publications-2002-1997>

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